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Examiner Signature		Date Considered	7-22-04
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Sheet	2	of	2	Attorney Docket Number	REYNICK 2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Parent application Serial No. 09/558,130 filed on April 25, 2000 to Joseph A. Reynick, entitled "INTEGRATED CIRCUIT EARLY LIFE FAILURE DETECTION BY MONITORING CHANGES IN CURRENT SIGNATURES", allowed November 12, 2003.	
RK		Thibeault, "A NOVEL PROBABILISTIC APPROACH FOR IC DIAGNOSIS BASED ON DIFFERENTIAL QUIESCENT CURRENT SIGNATURES", 15th IEEE VLSI Test Symposium, 27 April-1 May 1997, pp. 80-85.	—
RK		Thibeault et al. "DIAGNOSIS METHOD BASED ON DELTA-IDDQ PROBABILISTIC SIGNATURES: EXPERIMENTAL RESULTS"; IEEE International Test Conference, 1998 (Month Unavailable), pp. 1019-1026.	—
RK		Thibeault, "ON THE COMPARISON OF DELTA-IDDQ AND IDDQ TESTING," VLSI Test Symposium, 17th IEEE Proceedings, 25-29 April 1999, pp. 143-150.	—
RK		Thibeault, "IMPROVING DELTA-IDDQ-BASED TEST METHODS", IEEE International Test Conference Proceedings, 3-5 Oct. 2000; pp. 207-206.	—

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